

CommonNetlistErrors

- Device powers or grounds missing. This is due to power/ground pins not specified in the schematic symbol properties.
- Power or ground nets fragmented. Conflicting net names in the schematic symbol properties.
- Signal nets fragmented. Missing links between sections of nets on multiple sheets.
- Casually-named device pins. Naming the ground pin of a 3-terminal regulator "GND" rather than "2" as it appears on the part data sheet.
- Inconsistent pinning conventions. The 1N4148 diodes have anodes numbered as pin 1, but the 1N5134 zeners use anodes numbered as pin 2.
- Incorrect pinning conventions. SOT23 packages have 3 pins and 6 possible ways to assign pin numbers to those pins. Only one way is correct. Merging a netlist based on incorrect pin assignments with a library footprint having the correct pin assignments will result in a mis-wired transistor or other device. On boards with multiple SOT23 packages, this could be a disaster. The correct way to assign pin numbers to a SOT23 is as follows: orient the package with the top side up and the single pin facing away. The two pins facing you are numbered 1 and 2 from left to right. The lone pin is numbered 3.